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Debra M. Szumowski  
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DOCKET: CU-3599

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

APPLICANT: Naosumi WAKI )  
SERIAL NO: 10/784,667 ) Group Art Unit: 2818  
FILING DATE: February 23, 2004 ) Examiner:  
TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT )  
APPARATUS )

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

REQUEST FOR CORRECTED FILING RECEIPT

Dear Sir:

The official filing receipt has been received from the Patent Office in the above application.

There are two errors therein as shown on the attached copy of the filing receipt.

1. This application was filed with five (5) sheets of drawings containing Figures 1-6. Attached is a copy of the return postcard, date-stamped by the U.S. PTO acknowledging receipt of five sheets of drawings.
2. Reference to the Japanese priority application has been omitted. Attention is directed to page 3 of the Combined Declaration & Power of Attorney.

It is believed that no fee is due since the correction is not due to any error by the applicant and issuance of a corrected filing receipt is respectfully requested.

Respectfully submitted,

Brian W. Hameder  
Date

/13

  
Attorney for Applicant

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/784,667	02/23/2004	2818	770	CU-3599 RJS	5-8	4	

CONFIRMATION NO. 1316

26530  
LADAS & PARRY  
224 SOUTH MICHIGAN AVENUE, SUITE 1200  
CHICAGO, IL 60604

## FILING RECEIPT

\*OC000000012704551\*

Date Mailed: 05/18/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Naosumi Waki, Atsugi-Shi, JAPAN;

## Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2003-174748 6/19/2003

If Required, Foreign Filing License Granted: 05/18/2004

Projected Publication Date: 08/25/2005

Non-Publication Request: No

Early Publication Request: No

## Title

Semiconductor integrated circuit apparatus

## Preliminary Class

365

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The Patent & Trademark Office acknowledges and has stamped hereon the date of receipt of the item checked below:

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- |   |   |
|---|---|
| <input checked="" type="checkbox"/> New pat. <del>design</del> appln. | <input checked="" type="checkbox"/> Assignment & Recordal Cover Sheet |
| <input checked="" type="checkbox"/> Transmittal letter SB/05          | <input checked="" type="checkbox"/> CC of priority document           |
| <input checked="" type="checkbox"/> Fee Transmittal (PTO/SB/17)       | <input type="checkbox"/> Application Data Sheet                       |
| <input checked="" type="checkbox"/> 9 pages spec./abstract            | <input type="checkbox"/> Amendment/Response                           |
| <input checked="" type="checkbox"/> 2 pages of 4 claims               | <input type="checkbox"/> Request for Extension                        |
| <input checked="" type="checkbox"/> 5 sheets of drawings              | <input checked="" type="checkbox"/> Fee \$810                         |
| <input checked="" type="checkbox"/> Declaration/Power                 | <input type="checkbox"/> Other _____                                  |

APPLICANT: Naosumi WAKI

SERIAL NO: \_\_\_\_\_

FILED: \_\_\_\_\_

TITLE: SEMICONDUCTOR INTEGRATED  
CIRCUIT APPARATUS

DOCKET: CU-3599 DATE: 02/23/2004

22278 U.S. PTO  
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